ATTY DOCKET NO. SERIAL NO. 10/085,086 12494-US INFORMATION DISCLOSURE CITATION APPLICANT(S) Denis GALLANT, et al. (Use Sp)oral sheets if necessary) FILING DATE GROUP 2633 1 March 2002 U.S. PATENT DOCUMENTS EXAMINER FILING DATE SUBCLASS DOCUMENT NUMBER DATE NAME CLASS 5,809,221 395 182.02 June 7, 1995 September 15, 1998 Kumar et al. TV FOREIGN PATENT DOCUMENTS TRANSLATION DOCUMENT NUMBER COUNTRY CLASS SUBCLASS YES NO 7) July 20, 2000 Ірст WO 00/42811 70 WO 00/67408 November 9, 2000 PCT December 17, 1997 EΡ 70 EP 0 813 349 A2 Tν WO 00/40040 July 6, 2000 PCT OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) "A 0.155-, 0.622-, and 2.488-Gb/s automatic bit-rate selecting clock and data recovery IC for bit-rate 70 transparent SDH systems", J. Christoph Scheytt et al. IEEE Journal of Solid State Circuits, Vol. 34, No. 12, December 1999, pages 1935-1943. DATE CONSIDERED **EXAMINER** 02/19/2005 Dzung *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy if this form with next communication to applicant.

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